

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

YASUHIKO KURIYAMA

: GROUP ART UNIT: 2811

SERIAL NO.: 09/817,216

FILED: MARCH 27, 2001

EXAMINER: CHOE, HENRY DOSY CENTER 28

FOR: SEMICONDUCTOR POWER

AMPLIFIER AND MULTISTAGE MONOLITHIC INTEGRATED

CIRCUIT

AMENDMENT

COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In response to the Office Action mailed January 28, 2002, please amend the aboveidentified application as follows.

IN THE CLAIMS

Please amend the claims as shown in the clean form, below. A marked-up copy is attached.

1. (Amended) A semiconductor power amplifier for amplifying a high frequency signal inputted to a signal input terminal by first and second transistors connected in parallel to each other to output the amplified signal via a signal output terminal, said semiconductor power amplifier comprising:

a first capacitor element connected between said signal input terminal and an input terminal of said first transistor;